

ACHILLES-DS LTD

GRADUATE FPGA ENGINEER / DIGITAL DESIGN ENGINEER

ACHILLES-DS

Build advanced FPGA and embedded systems for next-generation space and communications applications.

DIGITAL DESIGN

📍 Nicosia, Cyprus | 🏢 On-site | ⌚ Full-time | 📅 24-month contract

YOUR MISSION

Join our engineering team and contribute to the design, implementation, integration and validation of FPGA-based digital systems using AMD Xilinx technologies. You may also work on quantised neural-network accelerators and real-time DSP systems.

WHAT YOU WILL WORK ON

- FPGA architectures using VHDL, Verilog and/or HLS
- Custom IP integration and verification in Vivado
- AXI4, AXI-Stream, AXI-Lite and DMA interfaces
- Zynq ARM-FPGA integration and embedded Linux
- Timing closure, optimisation and hardware debugging
- Board bring-up and laboratory validation
- Technical documentation and engineering reports

TECHNOLOGY STACK

AMD Xilinx Vivado

VHDL / Verilog

Zynq

PetaLinux

Linux

Python / C++

AXI / DMA

DSP

FINN / Brevitas / ONNX

WHO WE ARE LOOKING FOR

- ✓ Recent BSc or MSc graduate in Electrical Engineering, Computer Engineering, Electronics or a related discipline
- ✓ Strong foundation in digital design
- ✓ Academic, internship or personal-project exposure to FPGA development
- ✓ Familiarity with VHDL, Verilog and/or HLS
- ✓ Basic experience with Vivado, C/C++, Python and Linux
- ✓ Good written and spoken English
- ✓ Curiosity, initiative and strong problem-solving skills
- ✓ Authorised to work in Cyprus

WHAT WE OFFER

- ★ Competitive graduate salary
- ★ Private medical insurance
- ★ Employer-funded technical training
- ★ Company laptop and engineering equipment
- ★ Friday team lunch, coffee, snacks and refreshments
- ★ Paid day off on your birthday
- ★ Hands-on work with advanced FPGA and embedded technologies
- ★ Possibility of extension based on performance and future projects

READY TO BUILD WHAT COMES NEXT?

Send your CV and a short cover letter to info@achilles-ds.com

achilles-ds.com

Nicosia, Cyprus